Amendments to the Specification

*Please replace paragraph **[0004]** with the following amended paragraph:*

[0004] Display image quality is further effected by ambient light surrounding the display, which can reduce the environments in which a user may feel comfortable using a battery powered device that adjusts the backlight to save power, which is especially important considering the self-contained battery power-source is one of the key factors facilitating mobility that allows the ~~use~~ user to move at will between different indoor and outdoor environments.

*Please replace paragraph **[0006]** with the following amended paragraph:*

[0006] The image adaptation technique described herein can be applied to a broad class of electronic systems having associated display devices. While the examples herein ~~a~~ are generally directed to laptop computers, the techniques described can be applied to personal digital assistants (PDAs), palm top computers, desktop computers using flat panel displays, kiosk displays, etc. Figure 1 is a block diagram of one embodiment of an electronic system. Electronic system 100 includes processor 102 coupled to bus 105. In one embodiment, processor 102 is a processor in the Pentium® family of processors including the Pentium® II processor family, Pentium® III processors, Pentium® 4 processors, and Pentium-M processors available from Intel Corporation of Santa Clara, California. Alternatively, different and/or other processors may be used, such as Intel's StrongArm processor, XScale processor, ARM processors available from ARM Ltd. of Cambridge, the United Kingdom, or OMAP processor (an enhanced ARM-based processor) available from Texas Instruments, Inc., of Dallas, Texas.

*Please replace paragraph **[0008]** with the following amended paragraph:*

[0008] MCH 110 may also include graphics interface 113 coupled to graphics device 130. In one embodiment, graphics interface 113 includes an accelerated graphics port (AGP) that operates according to an AGP Specification Revision 2.0 interface or PCI-Express Interface developed by Intel Corporation of Santa Clara, California. In another embodiment graphics device 130 may be integrated with MCH 110 forming a GMCH (Graphics and Memory Controller Hub). Other embodiments may be

possible such as when MCH 110 is integrated with the processor and Graphics Controller. In all cases Graphics Controller portion is referred to as Graphics Interface wherever contained therein.

*Please replace paragraph **[0021]** with the following amended paragraph:*

[0021] In one embodiment, image brightness agent 520 receives one or more signals from ambient light sensor 505 indicating the ambient light level in the environment in which flat-panel monitor 595 operates. Image brightness agent 520 determines a level of image brightness and/or backlight intensity modification that can be made based on the ambient light level and communicates the color changes and/or desired backlight intensity changes to backlight control agent 500. In one embodiment, backlight control agent 500 writes a value representing a scaling factor to backlight control register 540. As described in greater detail below, in one embodiment, the value stored in backlight control register 540 is combined with one or more other values to generate a duty cycle to control backlight intensity.

*Please replace paragraph **[0022]** with the following amended paragraph:*

[0022] In general an image to be displayed on flat-panel monitor 595 is communicated via display signals ~~505~~ 555, which enable timing controller 560 to activate appropriate column and row drivers 590 and 592, respectively, to display an image on flat-panel monitor 595. In one embodiment, blender unit 515 creates an image to be displayed on the display monitor by combining a display image with other display data, such as texture(s), lighting, and/or filtering data. These techniques are known in the art.

*Please replace paragraph **[0023]** with the following amended paragraph:*

[0023] In one embodiment, the display image from blender unit 530 and the output of gamma unit 545 are combined to generate display signals ~~505~~ 555, which are transmitted to timing controller ~~510~~ 560, as discussed above. Graphics gamma unit ~~545~~ 525 determines the brightness (luminance) of pixels in an image to be displayed by scaling each sub-pixel color. In one embodiment, graphics gamma unit ~~545~~ 525 can be programmed to scale the sub-pixel color on a per-pixel basis in order to achieve greater luminance in some areas of the display image, while reducing the luminance in other areas of the display image.

*Please replace paragraph **[0024]** with the following amended paragraph:*

[0024] In one embodiment, display image brightness indicators ~~550~~ 510 include data indicating image brightness determined by monitoring and accumulating pixel color within the display image. The display image brightness indicators can then indicate to image brightness agent 520 the brightness of certain features within the display image, such as display image character and background brightness.

*Please replace paragraph **[0029]** with the following amended paragraph:*

[0029] In a second mode of operation (first capture mode), the duty cycle of the PWM signal is determined by the value stored in backlight control register 540. The value stored in legacy backlight register 530 as well as any updates to the value do not directly or indirectly affect the PWM signal. In one embodiment, an interrupt is generated ~~with~~ when the value in legacy backlight register 530 is modified and the new value is stored in the register.